REMARKS

The specification has been reviewed, and clerical errors of the specification have been amended.

In the Action, claims 1-12 were rejected under 35 U.S.C. 102(e) or 35 U.S.C. 103(a) by Sueyoshi et al.

In view of the rejections, claim 1 has been amended, and claims 7-12 have been cancelled. Claims 2, 3, 5 and 6 have been editorially amended, and new claims 13 and 14 have been filed.

As clearly recited in amended claim 1, a control device for controlling an electronic device of the invention comprises a socket to be connected to an external memory, non-volatile storage memory means having a memory region with a portion where data can be electrically rewritten, and control means electrically connected to the socket and memory means for executing a control program to control an operation of the electronic device.

The memory means includes a first memory region for storing control program data for operating the electronic device, a second memory region for storing connection judgment program data to determine whether the external memory is connected to the socket, and a third memory region for storing writing program data to rewrite the control program data stored in the first memory region.

The control means initially executes the connection judgment program data. When it is determined that the external memory is connected to the socket, the control means executes the writing program data so that at least a portion of the control program data stored in the first memory region of the memory means is rewritten to external memory medium data stored in the external memory.

In particular, when the control device is turned on, the control means judges if the external memory is connected to the socket, and if it is connected, the control program is rewritten to the external memory medium data. Thus, the specific device or

signal is not required to rewrite the program. The program can be easily written.

Sueyoshi et al. is directed to data for processing method and its apparatus, wherein a semiconductor circuit generates job management data including job execution order data showing an order of execution of a plurality of jobs forming processing in accordance with a processing request and status data showing the state of progress of execution of the plurality of jobs for each of the plurality of processing requests. One job management is selected and executed. A control circuit selects the job and executes. Namely, a SAM chip selects one entity data from a plurality of entity data, selects and executes the job to be executed next based on the status data and processing order data of said selected entity data, and updates the status data in accordance with execution of said job.

In the invention, the socket is used, to which the external memory is connected. The control means initially executes the connection judgment program data and judges if the socket is connected to the external memory, by which the control program is rewritten. In Sueyoshi et al., there is no socket, and program is not rewritten on the basis if the socket is connected to the external device.

In Sueyoshi et al., an external memory, IC card or other means is used and connected to the user, but these means do not contain a new program for changing the program used in the user. Therefore, Sueyoshi et al. does not have the control means of the invention. The control means of the invention initially, i.e. when the control device is turned on, executes the connection judgment program data, and when it is determined that the external memory is connected to the socket, the control means executes the writing program data so that at least a portion of the control program data

stored in the first memory region of the memory means is rewritten to external memory medium data stored in the external memory.

In Sueyoshi, the SAM chip selects entity data and executes the job based on the status data and processing order data. However, the specific processing of the invention now claimed is not disclosed.

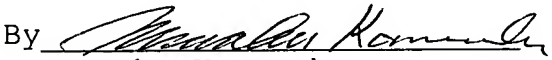
The features of claim 1 of the invention are not disclosed or even suggested in Sueyoshi et al.

Claim 14 is similar to claim 1, but the components thereof are limited to specific parts. Therefore, claim 14 is patentable as well.

As explained above, claims of the invention are patentable over Sueyoshi et al.

Reconsideration and allowance are earnestly solicited.

Respectfully Submitted,

By 

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